

# **[METHOD AND SYSTEM FOR MANAGING ELECTRICAL SCHEMATIC DATA]**

## **Abstract of Disclosure**

A method and system for managing electrical schematic data has been disclosed. The method includes the step of generating three separate schematics for an electrical part, a logical schematic, a layout schematic, and a physical schematic. These schematics are then stored together in part master file such that they may not be independently retrieved from a computer network. A computer utility manages the master file such that only one user may modify the part master file at a time. Additionally, the computer utility tracks all of the modifications to the part master file and saves all versions and revisions. If a change has been made to a part master file that would affect a second part master file, users will automatically be notified by the computer utility that a change has been made.

## Figures

Figure 1: A line graph showing the relationship between the number of hours spent on a task and the number of errors made. The x-axis represents 'Hours' (0 to 10) and the y-axis represents 'Errors' (0 to 10). The data points are as follows:

Hours	Errors
1	2
2	3
3	4
4	5
5	6
6	7
7	8
8	9
9	10
10	11